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250 225 200 175 150 125 100 75 50 25 0 100 90 80 70 60 50 40 30 20 10 0.1 1 10 100 1000 I - Load Current - mA O Efficiency - % Power Loss - mW V = 3.6 V, V = 1.8 V I O Efficiency PFM/PWM Operation

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The TPS82084 datasheet lists a typical ramp time of 0.8 ms, but gives no minimum value. Please could you tell me what the minimum... Please could you tell me what the minimum... Takashi Onawa

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from 0.8 V to 5.5 V in 25-mV steps. Contact Texas Instruments Sales for specific voltage option needs. Device Information(1) PART NUMBER PACKAGE BODY SIZE (NOM) LP5912 WSON (6) 2.00 mm × 2.00 mm (1) For all available packages, see the Package Option Addendum (POA) at the end of this data sheet. space space space space space Simplified Schematic

~~LP5912 500-mA Low-Noise, Low-IQ LDO datasheet (Rev. D)~~

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