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The Kintex®-7 Embedded Kit includes the components of the Kintex-7 KC705 Base Evaluation Kit plus all additional soft content that embedded designers need to quickly design their high-performance embedded systems. This includes the embedded targeted reference design and relevant tools such as Vivado® Design Suite. Key Features & Benefits

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The targeted reference designs (TRDs) are built around a MicroBlaze soft processor with various peripherals to enable embedded applications. Two designs are provided as part of the TRD: the KC705_System (BIST system) and the video demonstration system.

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The Kintex-7 FPGA KC705 Evaluation Kit includes all the basic components of hardware, design tools, IP, and pre-verified reference designs including a targeted design enabling high-performance serial connectivity and advanced memory interfacing.

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Kintex-7 FPGA Embedded Kit TRD v2.0 for ISE Design Suite 14.1 with CES Silicon In order to

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set the environment variables, namely XILINX and XILINX_PLANAHEAD, it is required that settings32.sh or settings64.sh be sourced from the ISE installation folder.

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The Kintex-7 FPGA Embedded Kit is priced at \$1895 and is open for order entry. For more information or to download the Kintex-7 FPGA Embedded Targeted Reference Design, please visit www.xilinx.com/k7embkit. Customers can also contact their local Xilinx sales representative.

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The Kintex®-7 FPGA Connectivity Kit is a 20Gb/s platform for high-bandwidth and high-performance applications containing all the necessary hardware, software and IP to power quickly through your evaluation and development of connectivity systems. This includes a 20Gb/s targeted reference design featuring PCI Express, a DMA IP core from Northwest Logic, 10GBase-R, AXI, and a Virtual FIFO ...

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