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The book starts with an overview of Linux and the IA-64 architecture and then discusses each major subsystem of the kernel in more detail. Among others, there are separate chapters on how multi-tasking maps onto the underlying hardware, on the virtual memory subsystem, device support (programmed I/O, DMA & interrupts), symmetric multi-processing (SMP), and on the bootstrap

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The Linux kernel on IA64, includes a subsystem called perfmon to provide access to the IA-64 PMU. The perfmon interface consists of a single system call, perfmonctl () which supports a set of...

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IA-64 Linux Kernel: Design and Implementation | Semantic Scholar. From the Book: PREFACE This book grew out of the simple desire to describe exactly how Linux works on an IA-64 machine. By realizing that desire, we hope not only to shine a light on the inner workings of Linux, but also to share some of the excitement and the creative processes that are involved in solving the many technical challenges that arise when designing an operating system for a platform as radical and innovative as ...

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#### *IA-64 Linux Kernel: Design and Implementation | September ...*

Authoritative and complete description of the Linux kernel for IA-64/IPF (Itanium Processor Family) Provides an in-depth discussion of the ideas and concepts behind the Linux hardware abstraction interfaces, the IA-64 design space, and their actual realization From the lead architects and developers of the IA-64 Linux kernelThe IA-64 architecture and Itanium processors are designed to

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IA-64 is a high-performance microprocessor architecture co-designed by HP and Intel. The titanium chip is the first implementation of this new architecture. Intel already has outlined a roadmap of several chips to follow, such as the more powerful McKinley and Deerfield implementations.

#### *Mosberger & Eranian, IA-64 Linux Kernel: Design and ...*

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*IA-64 Linux Kernel: Design and Implementation by Mosberger ...*

Among the Linux professionals, the book should be of most direct value to general kernel programmers, authors of device drivers, and application programmers interested in tuning performance for IA-64. Beyond that, the descriptions of the Linux hardware abstraction interfaces are valid independently of the target platform.

*IA-64 Linux Kernel | Guide books - ACM Digital Library*

In IA-64 Linux Kernel: Design and Implementation, the kernel project's leaders systematically present every major subsystem, introducing interfaces used by Linux to abstract platform differences, showing how these interfaces are used in IA-64, and illuminating key issues associated with Linux kernel operation on any platform.

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The authoritative guide to the IA-64 Linux kernel implementation. Authoritative and complete description of the Linux kernel for IA-64/IPF (Itanium Processor Family) Provides an in-depth discussion of the ideas and concepts behind the Linux hardware abstraction interfaces, the IA-64 design space, and their actual realization

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4.7.2 IA-64 implementation . On Linux/ia64, switching the address space involves three simple steps: Set current page-table pointer to the global directory of the new address space. Ensure that the new address space has a valid ASN (region ID) by calling `get_mmu-context()`.

*Switching Address Spaces | Virtual Memory in the IA-64 ...*

Kernel Identify Mappings ¶. Linux/ia64 identity mappings are done with large pages, currently either 16MB or 64MB, referred to as "granules.". Cacheable mappings are speculative [2], so the processor can read any location in the page at any time, independent of the programmer's intentions.

*Memory Attribute Aliasing on IA-64 — The Linux Kernel ...*

From the lead architects and developers of the IA-64 Linux kernel The IA-64 architecture and Itanium processors are designed to offer unprecedented levels of performance, expandability, and reliability?and with the delivery of the IA-64 Linux kernel, that power is now accessible to every Linux developer.

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